

Voltage Control Techniques in Uninterruptible Power Supply Inverters: a Review

Ghazanfar Shahgholian, Jawad Faiz, Masoud Jabbari

Abstract – Uninterruptible power supply (UPS) systems are required for supplying sinusoidal output voltage for linear and nonlinear loads. They must be highly reliable and fast in dynamic response. Many control strategies have been applied to UPS inverters. The basic objectives of UPS control systems are tracking ability and robustness. Generally, the tasks of output voltage control for UPS inverters are providing fast dynamic responses and maintaining a perfect sinusoidal voltage waveform even with nonlinear or changing loads. To achieve these aims, many controllers have been proposed in the literature. In this paper a comprehensive review of the control techniques of UPS systems with advantages and disadvantages of each is carried out. Copyright © 2011 Praise Worthy Prize S.r.l. - All rights reserved.

Keywords: Inverter, Uninterruptible Power Supply, Control Techniques

I. Introduction

The main objective of uninterruptible power supply (UPS) systems is to supply a sinusoidal voltage with constant amplitude and frequency to critical loads such as industry controllers, computer and communication systems without any interruption and irrespective of load and supply conditions [1], [2]. It is well known that the main control objective in an UPS inverter is the tracking of the delivered voltage towards a desired sinusoidal reference in spite of the presence of distorted loads [3], [4]. UPS systems can be classified as static, rotary and hybrid. Obtaining high performance, such as low total harmonic distortion (THD), good voltage regulation and quick transient response for sudden changes at load, is very important in such applications [5]-[8]. The block diagram of a typical on-line UPS inverter system is shown in Fig. 1.

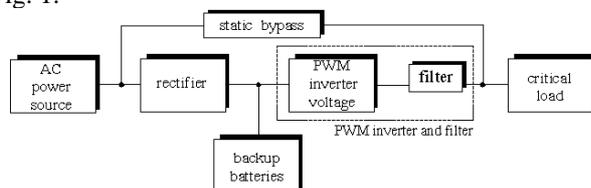


Fig. 1. Block diagram of a typical on-line UPS system

A rectifier is used for converting single-phase or three-phase alternating ac input into direct dc, which supplies both the battery bank for energy storage and voltage source inverter. A half bridge or full bridge pulse width modulation (PWM) inverter is used to convert a dc voltage to a low THD sinusoidal output, a battery to provide continuous source of electrical power and a power LC filter to reduce harmonics or ripple from the inverter output [6]-[12].

The UPS system has two operating modes. Without the input power, the static bypass switch is opened and the charger is disabled. The inverter operates in backup mode and supplies power to the load using the battery [7]. During the bypass mode, when the ac line is within the preset tolerance, most of the power is supplied directly from the ac line to the load. It typically operates with a PWM strategy under feedback control to realize the desired output voltage, which minimizes the filter cost, size, weight and loss. It is also used to overcome the voltage fluctuations of power system [8]. Some of the main objectives of UPS are [9], [10]: having high efficiency and reliability, providing a regulated sinusoidal output voltage with minimal THD, having sinusoidal input current with low THD and unity power factor over normal mode, wide input voltage fluctuations with constant output voltage, capability of smooth transfer from charging mode to backup mode at the power fault and low noise at the input and output terminals.

Different techniques and circuits have been so far recommended to improve UPS performance and obtain ideal specifications [11]-[19]. In [12] the design consideration and digital control technique of an on-line, low-cost, high performance and single-phase UPS system based on a boost integrated fly-back rectifier/energy storage dc/dc converter is proposed. This controller follows the reference current and voltage of the inverter with a delay of two and four sampling periods, respectively. A feedback linearization approach based on pole placement technique to control the output voltage control of three-phase UPS systems is proposed in [13]. In [14] a control scheme using predictive control for a two-level converter is presented, which a cost function is used for selecting the switching state and an observer is used for load-current estimation. A feedback linearization technique

base on linear control theory to control of the output voltage of three-phase UPS systems is proposed in [15], which the tracking control law is obtained with a pole placement technique.

A hybrid regenerative power system including photovoltaic and wind powers and combining the functions of the grid tie system and UPS for critical load applications is presented in [16]. A droop method based on proportional-resonant controller to control the power sharing of parallel UPS systems is presented in [17], which ensures good transient response and steady-state objectives. In order to obtain fast response in the current control in single phase UPS, the digital control scheme based on the instantaneous values is proposed in [18], where in the controller is constructed by a DSP.

In [19] inner-outer loop controllers are adopted to regulate output voltage and to improve system response, and a current weighting distribution control (CWDC) strategy is used in multi-inverter systems to achieve current sharing is presented. In [20] UPS system with two LC filters in the inverter output is analyzed and their effects on reducing the distortion in the output voltage is shown. The system model consists of the output filter, the control system and the single phase inverter. In [21] a model of UPS system has been introduced based on series and parallel combination of two full bridge voltage inverters where a series converter receives the input and a parallel converter is the load. Connected to wide applications of UPS systems have extended the control strategies to achieve performance and pure sinusoidal output voltage. Output impedance, transient response for nonlinear load and load changing, voltage regulation and THD factor are important operational parameters for a UPS system. Inverter provides a sinusoidal output voltage for nonlinear and discrete loads and the objective of the design of control system is to supply voltage for load variations. There are three non-linearities in PWM voltage source inverters [22]: (1) the dead time derates the output voltage and the influence is dependent on the direction of phase current, (2) a ripple in the dc link voltage, due to the behavior of the rectifier, influences directly on the output voltage, and (3) the output drop across the switches influences the output voltage. Performance of a PWM is evaluated by THD factor; this factor expresses the reliability of the system. To develop a sinusoidal output voltage with low distortion in UPS system, a powerful controller is required for closed-loop regulation in inverters. In spite of existing high frequency switching devices, a good sinusoidal output voltage is realized using a PWM inverter with LC filter. The closed loop control has particular application in the UPS system. Varying load and non-ideal PWM inverter cause a problem of having a low THD factor for output waveform and simultaneously not having a good transient response [23]. A PWM technique used for eliminating the harmonic components cannot prevent the distortion of output voltage in the nonlinear loads. Control methods require similar data from parameters of the system. Also

the switching frequency must be high. In the UPS applications, switching frequency is chosen tens of kHz that is very larger than natural and PWM inverter system modulation frequencies.

The harmonics lead to communication interference, excessive heating in capacitors and transformers, solid state device malfunctions, and so on. The main drawback of PWM inverter is the large size, considering nonlinear loads such as rectifier and triac loads. Nonlinear loads and parameter uncertainties, cause periodic tracking error, are major sources of THD in UPS system [24]. Multiple filters provide an alternative to minimize periodic error occurred in a dynamic system. In [25] describes the design procedure of the inverter output multi filter. With multi filter, the output voltage waveform can be sinusoidal under nonlinear load, no load or light load. This approach requires only sensing of the output voltage.

A typical control system is composed of four parts: a plant to be controlled, sensors for measurement, actuators for control action and a control law. Generally, the tasks of control systems can be divided into two categories: stabilization or regulation and tracking or servo. The basic tasks of the UPS control include: (1) load voltage magnitude and frequency regulation, (2) maintaining a sinusoidal voltage waveform at the load and (3) damping of output filter oscillations. Furthermore, for three phase UPS systems maintaining phase voltage balance and for parallel redundant UPS systems ensuring power sharing, should be considered [26]. To remove overall drawback, several techniques and different control strategies have been proposed in the literature as shown in Fig. 2 [27].

In this paper various control techniques to achieve both good dynamic response and low THD at output voltage of UPS inverter system are reviewed. The major difference between common objective from an advantages and disadvantages point of view are described. The paper is structures as follows. Section II presents a general review of classification control techniques. The learning feed forward controls such as repetitive control, instantaneous feedback controls such as deadbeat control and nonlinear controls such as sliding control are described in sections III, IV and V, respectively. Finally, this paper concludes with a brief outline of the advantages and disadvantages of various control strategies in section VI.

II. Classification Control Techniques

Based on the control system objectives, feedback control schemes for UPS inverters may be classified as follows.

II.1. Continuous-Time and Discrete-Time Control

Feedback control strategies devised for UPS inverters can be broadly classified as continuous-time control (CTC) and discrete-time control (DTC).

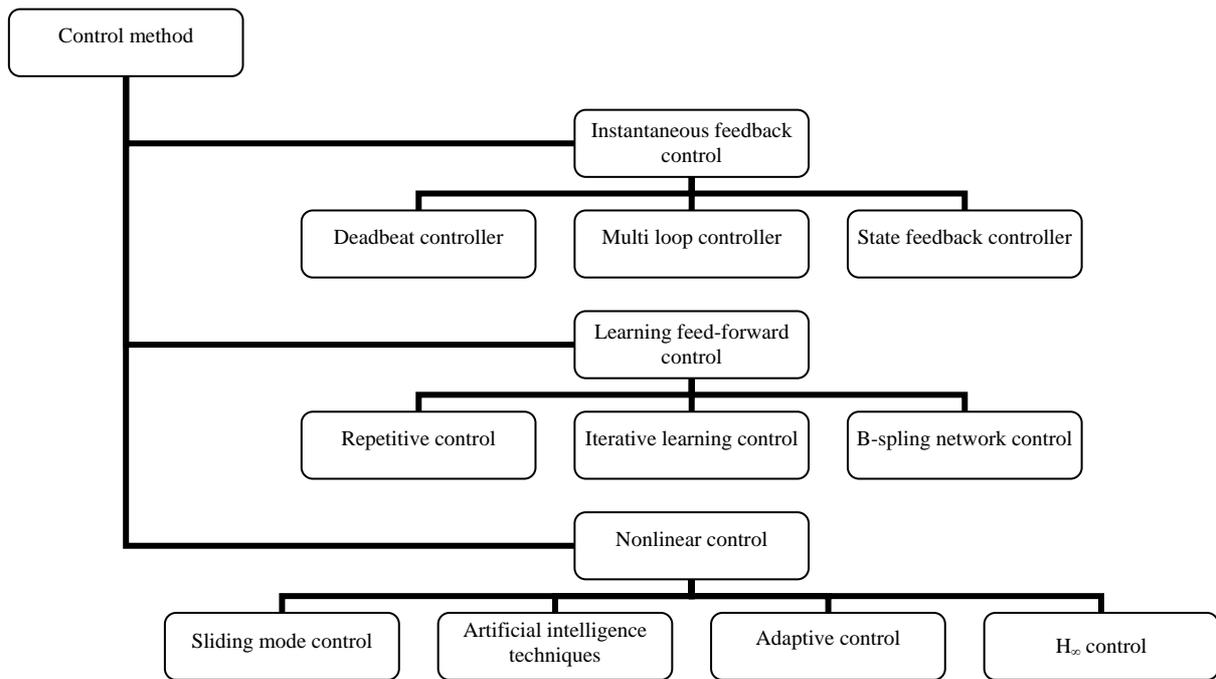


Fig. 2. A classification of control techniques for UPS inverter system

With advent of fast microcontrollers, DTC strategies have been proposed. The response time of such schemes are limited by microcontroller speed and give rise to considerable distortion with nonlinear loads. CTC strategies are much faster and can lead to much less distortion [10], [28]. In [29], an optimal control method based on the linear quadratic regulator (LQR) approach is proposed in continuous-time for single phase UPS inverter. Discrete-time LQR technique with repetitive controller is proposed in [30], which the LQR parameters are calculated by minimizing a cost function.

II.2. Analog and Digital Control

The controllers can also be classified into two groups: analog based such as multiple feedback loops [31] and digital based such as deadbeat controller [32], [33]. Analogue techniques are used in continues approaches. Most of the analog based controllers were designed based on linearized model and traditional frequency domain analysis.

In designing a digital controlled PWM switching converter, two switching frequencies require careful selection: the PWM switching frequency of the power converter and the sampling frequency of the digital controller [34]. Various digital control schemes for UPS inverters have been proposed in last twenty years, including deadbeat control, repetitive control, digital multi loop control, and so on. There are many advantages for digital controllers, such as immunity to drifts, insensitivity to component tolerances, ease of implementation and changeable control law software updating. If the poles of a closed loop system of an analogue control system are far from s -plane, the system has a quicker

dynamic response, but in the digital systems, all poles of the closed loop must be on the origin of z -plane [35].

II.3. Linear and Nonlinear Control

On the basis of design and analytic approach can be divided into two main groups: linear such as predictive control [36] and ramp comparisons current [37], and nonlinear such as the neural network [38], hysteresis current control [39], H_∞ control [40] and fuzzy logic control [41]. It appears that the nonlinear controller is more suitable than the linear type since the inverter is truly a nonlinear system. Various methods of current control can be used in UPS inverter to provide current protection, improve the performance of output voltage and to simplify parallel operation. The performance of current controller systems depends on the feedback control strategy used, which can be broadly categorized into linear and nonlinear systems [42].

III. Learning Feed-Forward Control

Feed forward is used to cancel the effects of known disturbances and provide prediction in tracking tasks. Learning control scheme is easy to implement and do not require exact knowledge of the dynamic model. In learning control, the controller is not designed on the basis of the process model. The controller is either trained based on the previously collected data or is trained during control. The learning feed forward control (LFC) acts as an add-on element to the existing feedback controller. In LFC there are two parameters B-spline support width and the learning gain to adjust [43]. A plug-in digital repetitive learning control scheme for

three phase constant-voltage constant-frequency (CVCF) PWM inverter is present in [44]. Several feed forward control techniques have been proposed: modulated integral control [45], modified discrete control law [46], one cycle control [47], and so on. In [48], a reset integral controller was developed for a single phase UPS inverter by using one cycle control technique. This approach was particularly suitable for its robustness against large input voltage variations. However, the performance of the output voltage decreases when nonlinear load must to be supplied.

III.1. Repetitive Control

Repetitive control (RC) can be regarded as a simple learning control because the control input is calculated using the information of the error signal in the preceding periods. Repetitive controller is a learning controller that uses the information of the output error in the previous cycles to compute the repetitive action. RC is one of attractive methods in the practical applications due to the simplicity of its algorithm and of independence varying samples from the output voltage. If the repetitive control is directly combined with PWM inverter, it can generate a good quality voltage with minimum cost. When the reference signal has harmonic components in the order of the fundamental frequency, the RC improves the steady state response of the system. This method is easily applied and only requires the measured output voltage but the dynamic response of the system is slow [49]. Designing a repetitive controller for open loop SPWM is not easy due to the inverter dynamic particularly in the no-load mode. Repetitive controller needs a complicated compensating network capable to sample the load periodically. The major objective of the RC is the use of repetitive nature in disturbances and faults and compensating the output voltage at any cycle. This method is not suitable during non-periodic transient modes such as switching. If a good dynamic during switching is required, an instantaneous feedback control is used. A conventional feedback controller is utilized to increase the stability margin of the closed loop systems.

Fig. 3 shows the block diagram of a repetitive controller for ac voltage regulation. A repetitive controller consists of three tracking controller (G_1), continual controller (G_2) and parameter tuning controller (G_3). When the controller is used to eliminate the oscillating or periodical disturbances, controller is employed for improvement of the transient mode response. The adaptive algorithm estimates the parameter G_3 of the system parameters and the continual controller is regulated for stability assurance and automatic quick elimination of the periodical disturbances over all frequency modes [50]. So far, many strategies have been employed for RC in industry. In [1], the RC has been combined with pole placement technique. In this method, parameter changes because a small change in the pole places. Designing pole transfer controller is easy, because it does

not need a quick response and minimal steady state error simultaneously. In [51], a reference model controller with RC for UPS has been recommended based on the least square error method which is stable for a wide range of the filter parameters. In [52], a repetitive learning controller is used to obtain high quality output waveform from an inverter feeding a non linear load. In [53] an adaptive repetitive control scheme that employs an auxiliary compensator to stabilize the closed loop system even with variations in the plant is presented. In [54], a robust model reference adaptive controller is presented including a repetitive control for UPS applications. It can effectively eliminate periodic waveform distortion resulted by unknown periodic disturbances, and is globally stable in the presence of un-modeled dynamics.

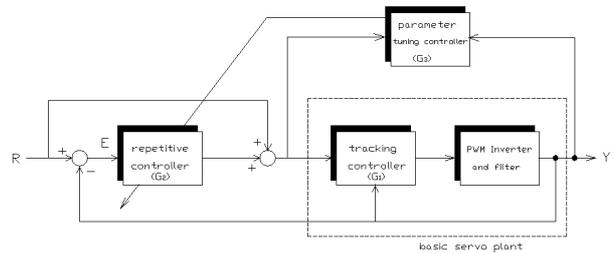


Fig. 3. Repetitive controller

III.2. Iterative Learning Controller

The iterative learning controller (ILC) provides a solution for minimization of periodic errors due to nonlinear loads, especially for low frequency harmonic components. ILC is implemented through memory based learning approach. The ILC as shown in Fig. 4 is comprised of feed forward learning loop from previous learning cycle error and feedback learning loop from instant error.

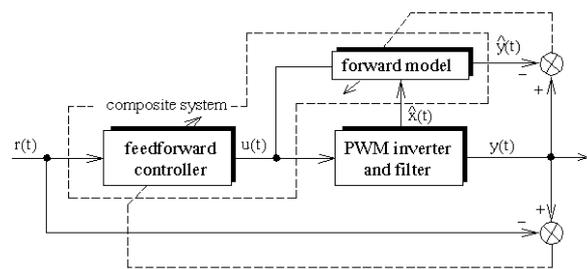


Fig. 4. Block diagram of iterative learning controller

This control scheme learns a feed forward signal as a function of the time. A drawback of the ILC scheme is that it can only be applied if the task is repetitive. The ILC problem setting is very similar to the RC case, but the only difference is that whereas in ILC the state of the system is reset to initial condition at the end of the period, in RC the initial condition for each period is the final state from previous period [55]. A current regulation method based on iterative control technique is propos-

ed in [56], in order to reduce the distortions caused by both the dead-time and the zero crossing problems. In [57] an ILC scheme in discrete time domain for an inverter system used in ac power sources minimizes periodic errors caused by both linear load and non linear load.

III.3. B-Spline Network Control

Spline theory is used to solve problems associated with large amount of data or information needed to be described in a simple form. The B-spline functions can be used to smoothly fit the data given, and are widely used in the engineering fields. Local adjusting is easy and needs simple calculation and implementation [58]. B-splines functions are functions with minimum local supports in all splines functions. B-splines allow describing spline functions as linear combinations of weighted basis functions.

They are numerically stable and simplify the computation of spline functions. B-spline network control (BSN) is a specific realization of an associative memory network [59]. B-spline neural network is characterized by a local weight updating scheme with the advantages of fast convergence speed and low computation complexity. B-spline neural network is more suitable for real time applications [60].

IV. Instantaneous Feedback Control

The instantaneous feedback control (IFC) techniques have been applied to improve the dynamic transient response, obtain output voltage with low THD and improved disturbance rejection via lower output impedance. This approach has the disadvantage that harmonics are generated in the output voltage at frequencies around the switching frequency [27]. Many fast response IFC strategies such as deadbeat control and cascade control have been developed for distinct applications to achieve zero steady state error and fast transient response which can occur under nonlinear loads.

IV.1. Deadbeat Controller

Digital system controller is used in the design of inverter systems in order to obtain a suitable response against sudden change of the loads. In a digital control system, the signal over one or many points is expressed as a numerical code in a digital converter. Instantaneous feedback control techniques using dead beat control are for improving the transient response and compensating the PWM inverter waveform [48], [61]. DTC strategies are mostly based on the deadbeat control (DBC) theory. DBC was introduced in the middle of decade 1980 [22], [62]. In DBC technique, a CTC system is converted into a discrete system and a DBC from output variable over minimum sampling time is obtained by applying a suitable feedback. This method is the best way for obtaining a quick response in the control of inverter voltage, but it

is very sensitive in applying to a UPS system. DBC, which originated from states equations, has very fast dynamic response and can eliminate voltage variation in several control periods [63].

Also for a full digitalizing, UPS systems with low capacity are used. In this method, the design objective is to minimize the maximum jumping and making quickly the rise time of the response. Contrary to analogue control systems, the response for digital control systems is unique and system is only adjusted for the designed input, and it does not show a good performance against other inputs and has high sensitivity to the load change [64]. The technique of locating all poles of a discrete time system at zero equals DBC method. In [65], a digital signal processor in a UPS system with inverter has been given for providing a sinusoidal waveform; the control design consists of regulator for load rms current and voltage, in order to make ineffective the output voltage harmonics in a stable system. To achieve a desirable dynamic response and output voltage independent of the load change or parameters, digital multiple feedback control (DMFC) technique has been used for control of PWM inverter; this increases the cost of software and hardware parts [66], [67].

A DMFC has been suggested for a single phase half bridge in a UPS system in which control is based on the measurement of the capacitor filter voltage and load current, and the observed mode is used for prediction of the capacitor current. In [68], the current digital control system has been presented based on two observers and one compensator and stability of the system has been studied against parameters variations. A deadbeat controller with repetitive integral action suitable for UPS to achieve a deadbeat dynamic response for the controlled variables is presented in [69]. Fig. 5 shows basic block diagram for deadbeat controlled PWM inverter in UPS systems.

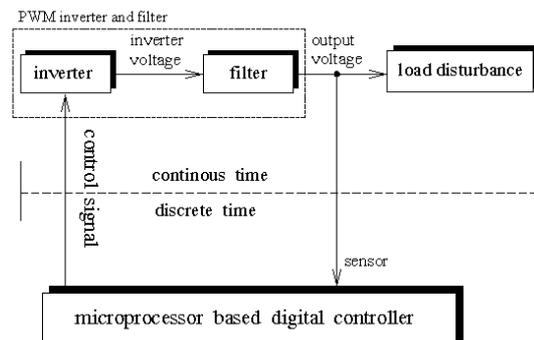


Fig. 5. Block diagram for deadbeat control of PWM inverter

IV.2. Multi-Loop Controller

The multiple feedback loop controller is simple and easy to implement. Using instantaneous feedback control results in sinusoidal input current and output voltage. The analogue control feature in UPS systems provides sinusoidal output voltage and input current with a high

input power factor. This type of control has low reliability and regulation due to change of regulation parameters. In the feedback control technique of instantaneous voltage, the reference output waveform is adjusted by comparison of feedback voltage with sinusoidal reference.

In such a case, the dynamic characteristics are quickly improved, since only the voltage signal is controlled, it is not optimal for current change and nonlinear loads and multiple loop control is applied. Current mode control is basically a multiple loop control method in which the current negative feedback loop is commanded by the error signal of the outer voltage regulation loop. Current regulators can be implemented on an analog or digital platform [41]. Typical current regulators reported in the literature include hysteresis current control (HCC), predictive current control (PCC), adjacent state regulators (ASR), ramp comparisons control (RCC), SPWM current control, peak current mode (PCM) controllers and average current mode (ACM) controllers [70], [71], [72]. The advantages of ACM are constant switching frequency and improved noise immunity. HCC is simple in implementation, inherently limits the current and has a fast transient response, but the switching frequency depends on load characteristics and the hysteresis band [73]. Also, the gain of the hysteresis comparator is considered to be infinite and protection of the hysteresis controlled inverter is difficult owing to the random appearance of the gating signals [74]. PCC requires a good knowledge of load parameters, in addition to having the same calculation problem, but implementation of a practical system can be difficult and complex. RCC using a PI regulator has a long history of use, but has the disadvantage of steady state phase error between the target current and the output current, and also requires accurate tuning to suit load parameters. SPWM current control, is comparison to instantaneous current error with triangular waveform, not only maintains constant switching frequency but also provides fast dynamic response for UPS application [75].

Fig. 6 shows the system control diagram of the current mode technique for single phase voltage-source full-bridge inverter with a mono filter. The output voltage feedback is compared with a sine reference signal and the error voltage is compensated by a PI regulator to produce the current reference (I_R).

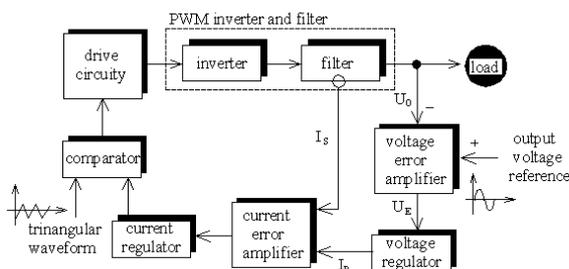


Fig. 6. Block diagram of current mode control with feedback

The instantaneous current control of inductance or capacitance filter (I_s) is sensed and compared with I_R . After compensated by a PI regulator, the error signal (U_E) is compared with a triangular waveform to generate SPWM signal for switching control of switches. The inductance current is the sum of capacitor and load currents and the load voltage is controlled in another closed loop [76]. The feedback reduces the system error; in addition, it influences band width, impedance, sensitivity and stability. In the advanced control system, three filter current signals are sensed as feedback in order to adjust the circuit current and output voltage, for regulating the load voltage and current and balance the load noises. In [77], a design of inverter using PWM technique with injecting harmonic to the three phase UPS systems, for changing dc bus voltage, has been proposed; and a control form with instantaneous voltage feedback and average voltage feedback for obtaining the static and dynamic characteristics have been presented. In [78], the inverter regulated current loop and output voltage balance have been suggested where an inner current circuit an external voltage circuit have been employed to make the produced un-damped poles ineffective by resonance circuit; the closed loop system will be stable, showing quick dynamic response. Also this paper has shown that the output current for feedback of UPS system with second-order load filter is not suitable. Here current or voltage of capacitor is used as the feed variable. The multiple loop control strategy which incorporates an outer voltage loop and an inner current loop is presented in [79], where inner current loop is added to regulate the output voltage of the PWM inverter. Also, the output voltage and load current compensation are added in the scheme to improve the performance and robustness of the system. The multi loop control (cascade control) with capacitor current inner loop and output voltage outer loop for a single phase half bridge UPS inverter is presented in [80]. In this approach, the transient performance of the controller totally depends on the high bandwidth of the inner loop, but the high gain of the controller would cause the closed loop system to be sensitive to noise.

IV.3. State Feedback Controller

To obtain a satisfying dynamic behavior of the closed loop system a state feedback applied in [81]. A powerful analytical method in state variables domain is used in the design of state feedback. The design of a stable feedback control system is based on a suitable selection of the feedback system structure. When all state variables are not achievable, the design output feedback. The typical architecture of applies feedback control system consists of a plant whose performance is controlled by an actuator. The actuator receives command signals from the controller, which calculates it in accordance with the respective reference input and feedback signals from the sensor [82]. In [83], a combination of state feedback

control and repetitive control is proposed. This hybrid achieves excellent dynamics and low THD with nonlinear loads, but the magnitude of the output voltage is subject to variations with load changes. In [80], a design procedure of a predictive digital state feedback control to ensure a sufficient quality of the output voltage under typical linear and nonlinear load conditions for a single phase ups inverter is shown. In [84], a robust state feedback control based on a linear matrix inequality design has been applied to a UPS system. In [85], repetitive control is combined with least square error (LSE) state feedback control, in the IFC scheme, serving as the inner loops.

V. Nonlinear Control

Nonlinear controllers generally present good dynamical response, robustness and stability. In nonlinear control, the concept of feedback plays a fundamental role in controller design, as it does in linear control. However, the importance of feed forward is much more conspicuous than in linear control. Very often it is impossible to control a nonlinear system stably without incorporating feed forward action in the control law. The use of nonlinear feedback makes the control system robust and less sensitive to load disturbances and output filter circuit parameter variations. Switching delays and loss limit the use of this control technique in low power single phase UPS inverters [86]. In [87], a nonlinear feed forward controller using one cycle based PWM generator and an output feed forward current is applied to a single phase UPS.

An improved nonlinear control based on the pole placement technique of the inverter output voltage for the three-phase UPS systems is proposed in [88], which it is shown that the proposed control scheme gives high dynamic responses at load variation as well as a zero steady-state error.

V.1. Sliding Mode Control

Sliding mode control (SMC), also called variable structure control (VSC), as a non-linear control technique was introduced in 1950. Basically, a SMC system is a switching control rule for guiding system to the designed modes with the relevant curve of the system. When a good transient response is required from output voltage, the equation of the sliding level in the space state is written by a linear combination of error of state variable. This error has been defined differently in various papers. In the SMC method, apart from the starting points in the state space, the system paths must be confronted with the sliding level and the movement of system on the sliding level must reach a stable point corresponding to the required voltage and current. The method provides a systematic balance for preserving the stability. This method is not sensitive to the variation of the parameters of the system and external disturbances. The main pro-

blem with this method is the system indifference to the unknown parameters and external disturbances. The major obstacle for the application of the SMC in inverter is the diversity of the switching frequency for the switch that produces a large amount of noises with high frequency and THD [89]. Feed controllers gain which is generally constant, varies in respect to the state variables in the SMC. Fig. 7 shows the discrete feed forward SMC scheme [90], where the control force (U) is composed of two parts: a feed forward control force (U_F) and an SMC force (U_S).

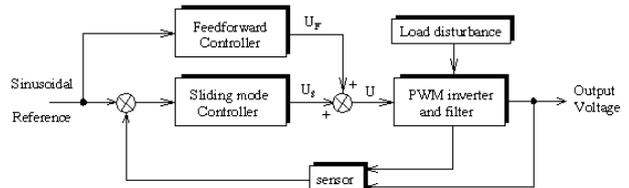


Fig. 7. Discrete feed forward sliding mode control scheme

The SMD design steps could be summarized as [91]: (1) proposing the sliding surface, (2) verifying the existence of a sliding mode and (3) analyzing the stability in sliding surface. Many papers have been published in the field of sliding mode control. A two level PWM inverter with fixed switching frequency and current limiter is proposed in [92], the overall performance is good, but two current measurements are required for the load and filter inductor currents, so it is not attractive from the cost and control points of view. An SMC is proposed in [93] where a periodic disturbance signal is added to make a pulse to pulse limit of the sliding surface function into low bound. It has the advantages of fixed switching frequency, current limiting and no additional load current measurement, but the load current observer will increase the circuit complexity. In [94], a discrete time SMC algorithm for UPS inverter has been presented based on a two loop design in which the effect of load current and inductance of filter for common control of PWM inverter has been used.

V.2. Artificial Intelligence Techniques

Many Artificial intelligence techniques such as neural network and fuzzy system have been employed to improve the controller performance for a wide range of plants while retaining their basic characteristics.

a. Fuzzy logic control: The regulation characteristic of a fuzzy controller is different from the linear controller because the fuzzy logic control (FLC) is mostly nonlinear and makes a lot of adjustment possible. Most simple fuzzy feedback control systems contain a FLC in the form of a table of linguistic rules and input-output interfaces. FLC has the potential of operating successfully under a wide range of load variations since their working principles do not require precise knowledge of the load parameters [95]. FLC can handle nonlinearity and does

not need accurate mathematical model. FLC is adaptive in nature which gives it robust performance under parameter variations and load disturbances [96]. A typical fuzzy process can be divided into four steps: the fuzzification, rule base, the inference mechanism and the defuzzification [97]. Fig. 8 shows a block diagram of fuzzy logic controller. The inputs of the fuzzy proportional-derivative (PD) controller are the error and the change of the error. The output of the fuzzy PD controller is the gain controller. A FLC is a synthesis of both, a controller loop and a set of linguistic rules which are the content of the decision element of the controller. FLC can work with less precise input. The algorithm is simple and it doesn't need advanced processor. It needs less data storage in the form of membership functions and rules compared to conventional look up table. The fuzzy controller is able to reduce both the overshoot and extent of oscillations and for improving the steady state response; the repetitive control as shown in Fig. 9 is used.

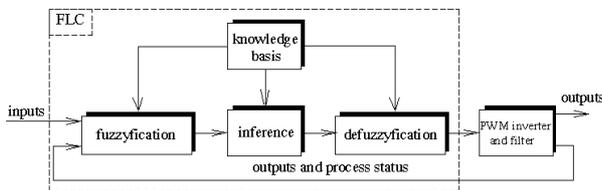


Fig. 8. Block diagram of the fuzzy logic controller

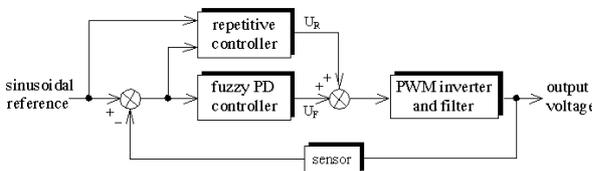


Fig. 9. Block diagram of the fuzzy-repetitive control

The fuzzy PD controller plays an important role in improving overshoot and rise time response during severe perturbations. A control system for UPS inverter includes double loop current mode control scheme in core and proportional-integral (PI) parameters of voltage control loop are adjusted using FLC presented in [98]. The hierarchical FLC scheme is employed for a single phase voltage source half bridge UPS inverter with second filter proposed in [79]. An approach for combining the deadbeat control and fuzzy logic compensator for real time digital of the single phase PWM UPS inverter is proposed in [99]. A hybrid fuzzy-repetitive control scheme for single phase CVCF is presented in [94]. An FLC and a digital PI control for the application in feedback control of a power factor corrected pre-regulator used in a high performance on-line UPS is proposed in [96].

b. Neural network controller: A neural network (NN) is an interconnection of a number of artificial neurons that simulates a biological system. When a NN is used in

system control, the NN can be trained either on-line or off-line [37]. The main advantage of the NN is that it has excellent merit for nonlinear control and is adaptive enough to firing the environment change.

Fig. 10 shows the proposed artificial neural network (ANN) of a 5-3-1 structure controlled inverter. The inputs are the capacitor current, delayed capacitor current, the load current, the output voltage and the error between the reference voltage and the output voltage [100].

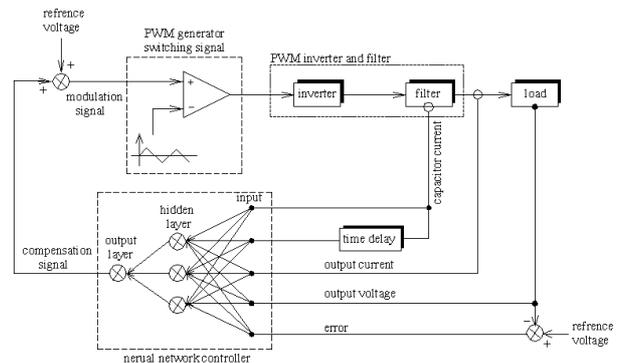


Fig. 10. Neural network control scheme for UPS inverter

The ability of the ANN to approximate nonlinear functions is most significant. A low cost analog ANN control scheme for UPS inverters with a selected ANN is trained off-line with the database comprising all example patterns is proposed in [101]. In [102] presented an ANN application in the harmonic elimination of PWM converters where the ANN replaced a large and memory demanding look-up table to generate the switching angles of a PWM converter for a given modulation index.

V.3. Adaptive Controller

The basic idea of adaptive control is to estimate the uncertain plant parameters online based on the measured system signals, using the estimated parameters in the control input computation. An adaptive control system can thus be regarded as a control system with online parameter estimation. The adaptive controllers are digital feed forward controllers containing gain coefficients that are updated by a learning process designed to optimize the controller response based on desired performance criterion.

The adaptive controller consists of two distinct parts: a feed forward control function which inputs load currents and voltages, and an on-line controller learning process which adjusts the feed forward controller gain coefficients with the objective of improving the controller performance. In [103], linear and nonlinear adaptive control strategies for a three phase UPS inverters are presented. An on-line adaptive learning algorithm is also described which promotes steady state controller stability.

TABLE I
A COMPARISON OF CONTROL TECHNIQUES

Control strategy	Advantage	Disadvantage
Multi loop control	- high robustness - ease of implementing	- more than two sensors are needed
Feed forward learning control	- require only sensing the output voltage	- transient performance is poor
Sliding mode control	- robustness and insensitive to parameter and load variations - fast dynamic response - simple implementation - intensive robustness	- very high switching frequency - large number of state variable sensors
Fuzzy logic control	- operating successfully under a wide range of load variation - explanations of results - fine tuning - simple structure	- very powerful processor - tolerance for ambiguity
Deadbeat control	- stable operation for disturbances and nonlinear load - very fast transient response - applicability to three-phase systems	- very sensitive to parameter and load variations - requires estimation of the load parameters - poor stability - requires a larger actuating signal to achieve the deadbeat effect
Repetitive control	- high quality sinusoidal output voltage	- can be implemented by fast microprocessor - very slow dynamic response - can be implemented by fast microprocessor
Neural network control	- can be trained either on-line and off-line - excellent merit for nonlinear control - generalization and learning ability -adaptive enough to fir the environment change	- tolerance for uncertainty - complex implementation
Instantaneous feedback control	- high quality output voltage - fast dynamic response	- more variables need to be sensed - high speed control is required

V.4. H_∞ Control

With the advances in the technology of microprocessors and digital signal processing, nonlinear digital control strategies such as H_∞ has been proposed for the control of UPS inverters. A key point in robust control is the definition of a suitable mathematical model of the uncertainties affecting the controlled plant [104]. The H_∞ control theory has been introduced in the early 1980s opening a new direction in robust control design. H_∞ control is able to theoretically take account of modeling errors, disturbances and system noises in design stage. The general configuration for H_∞ control is shown in Fig. 11, in which $\Delta(s)$ is output multiplicative uncertainty, $P(s)$ is the augmented plant obtained by appending the weighting function $W(s)$ to the output of the transfer function $T_{WU}(s)$ of the desired loop shapes. The symbol $T_{WZ}(s)$ denotes the closed loop transfer function from W to Z . The design goal is to synthesize the stabilizing controller $K(s)$ so that the H_∞ gain from W to Z is less than one. The advantages of this approach are reducing control circuit cost and simplified implementation, which requires only voltage feedback. An H_∞ loop shaping design for single phase UPS inverters to achieve sinusoidal tracking rather than set point regulation and good performance proposed in [39]. A robust controller based on the μ -synthesis for single phase UPS system is proposed in [9].

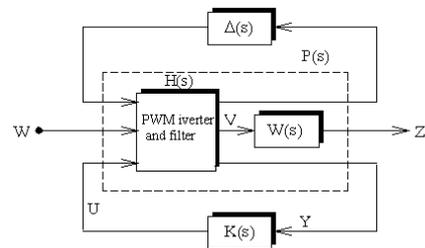


Fig. 11. Configuration of H_∞ control

VI. Conclusion

UPS systems are used in order to assure the continuity of supply for the critical loads. At the same time, good load regulation, fast transient load response and good switching frequency suppression is required. In most cases, the cost of control system increases with its complexity. In this paper some research has been carried out on the various control techniques of UPS inverter to achieve appropriate dynamic response and output voltage with low total distortion harmonic. Their advantages and disadvantages have been discussed. The multiple feedback loop controller requires a comprehensive analysis on both the open loop and closed loop frequency responses of the UPS inverter control system for various controller parameters. The dead beat control requires larger actuating signal to achieve the deadbeat effect and

the performance of the system is sensitive to parameter and load variations. Also, control signal depends on a precise PWM inverter load model. The characteristics of several control techniques for UPS inverters and the advantages and disadvantages are summarized in Table I. Modern UPS control systems are implemented digitally, on control hardware built around one or more digital processors.

References

- [1] F.S. Pai, S.J. Huang, A novel design of line interactive uninterruptible power supplies without load current sensors, *IEEE Trans. On Pow. Elec.*, Vol.21, No.1, pp.202-210, Jan. 2006.
- [2] J. Faiz, G. Shahgholian, "Uninterruptible power supply – A review", *ELECTROMOTION*, Vol.13, No.4, pp. 276-289, Nov./Dec. 2006.
- [3] G. Escobar, A.A. Valdez, J.L. Ramos, P. Mattavelli, Repetitive based controller for a UPS inverter to compensate unbalance and harmonic distortion, *IEEE Trans. On Indu. Elec.*, Vol.54, No.1, pp.504-510, Feb. 2007.
- [4] M. Niroomand, H.R. Karshenas, Performances specifications of series-parallel UPS's with different control strategies, *International Review of Electrical Engineering (IREE)*, Vol. 4, No. 1, pp. 14-21, Feb. 2009.
- [5] Y.H. Chen, P.T. Cheng, Flux estimation techniques for inrush current mitigation of line-interactive UPS systems, *IEEE Trans. on Ind. Appl.*, Vol.47, No.2, pp.901-911, March-April 2011.
- [6] L. Bouslimi, A. Chammam, M. Ben Mustapha, M. Stambouli, J.P. Cambronne, Simulation and experimental study of an electronic pulsed power supply for HID lamps Intended for photochemical applications, *International Review of Electrical Engineering*, Vol. 4, No. 5 (Part A), pp. 799-808, Oct. 2009.
- [7] Y.H. Chen, P.T. Cheng, An inrush current mitigation technique for the line-interactive uninterruptible power supply systems, *IEEE Trans. on Ind. Appl.*, Vol.46, No.4, pp.1498-1508, July-Aug. 2010.
- [8] R. Senthil Kumar, J. Jerome, P. Prem, T. Alex Stanley Raja, Soft switched four wire inverter for UPS applications, *International Review of Electrical Engineering*, Vol. 5, No. 4 (Part A), pp.1405-1412, Aug. 2010.
- [9] A.V. Jouanne, P.N. Enjeti, D.J. Lucas, DSP control of high power UPS systems feeding nonlinear loads, *IEEE Trans. Indu. Elect.*, Vol.43, No.1, pp.121-125, Feb. 1996.
- [10] E. Beser, B. Arifoglu, S. Camur, E. Beser, E. Kandemir, A novel design and application of a single phase multilevel inverter, *International Review of Electrical Engineering (IREE)*, Vol.4, No.1, pp.7-13, Jan. 2009,
- [11] H. Yahia, N. Liouane, R. Dhifaoui, Weighted differential evolution based PWM optimization for single phase voltage source inverter, *International Review of Electrical Engineering (IREE)*, Vol. 5, No. 5 (Part A), pp. 1956-1962, Oct. 2010.
- [12] J.H. Choi, J.M. Kwon, J.H. Jung, B.H. Kwon, High performance online UPS using three leg type converter, *IEEE Trans. On Indus. Elect.*, Vol.52, No.3, pp.889-897, June 2005.
- [13] H.F. Farahani, F. Rashidi, A novel method for selective harmonic elimination and current control in multilevel current source inverters, *International Review of Electrical Engineering (IREE)*, Vol. 5, No. 2 (Part A), pp. 356-363, April 2010.
- [14] T.S. Lee, K.S. Tzeng, M.S. Chong, Robust controller design for a single phase UPS inverter using μ -synthesis, *IEE Proc. Pow. Appl.*, Vol.151, No.3, pp.334-340, May 2004.
- [15] O. Kukrer, H. Komurcugil, N.S. Bayindir, Control strategy for single phase UPS inverters, *IEE Proc. Electr. Power Appl.*, Vol.150, No.6, pp.743-746, Nov. 2003.
- [16] G. Shahgholian, J. Faiz, M. Arezoomand, Dynamic analysis and control design of a single-phase UPS inverter with novel topology and experimental verification, *International Review of Electrical Engineering (IREE)*, Vol. 4, No. 4, pp. 513-523, Aug. 2009.
- [17] M. K. Rahmat, S. Jovanovic, K. L. Lo, Reliability and availability modelling of uninterruptible power supply systems using monte-carlo simulation, *International Review of Electrical Engineering (IREE)*, Vol. 1, No. 3, pp.374-380, Aug. 2006.
- [18] S. Iqbal, A low ripple and fast dynamic response voltage multiplier for X-ray power supplies, *International Review of Electrical Engineering (IREE)*, Vol. 5, No. 2 (Part B), pp. 766-772, April 2010.
- [19] M.K. Rahmat, S. Jovanovic, Reliability modelling of uninterruptible power supply systems using fault tree analysis method, *European Transactions On Electrical Power*, Vol.19, No.6, pp. 814-826, 2009.
- [20] A. Nasiri, Z. Nie, S.B. Bekiarov, A. Emadi, "An on-line UPS system with power factor correction and electric isolation using BIFRED converter", *IEEE Tran. On Indu. Elec.*, Vol.55, No.2, pp.722-730, Feb. 2008.
- [21] D.E. Kim, D.C. Lee, "Feedback linearization control of three-phase UPS inverter systems", *IEEE Trans. on Indus. Elec.*, Vol.57, No.3, pp.963-968, Mar. 2010.
- [22] L.G. Cortes, P. Ortiz, G. Yuz, J.I. Rodriguez, J. Vazquez, S. Franquelo, "Model predictive control of an inverter with output LCL filter for UPS applications", *IEEE Trans. on Ind. Ele.*, Vol.56, No.6, pp.1875-1883, June 2009.
- [23] D.E. Kim, D.C. Lee, "Feedback linearization control of three-phase UPS inverter systems", *IEEE Trans. on Ind. Ele.*, Vol.57, No.3, pp.963-968, March 2010.
- [24] H.C. Chiang, T.T. Ma, Y.H. Cheng, J.M. Chang, W.N. Chang, "Design and implementation of a hybrid regenerative power system combining grid-tie and uninterruptible power supply functions", *IET Rene. Pow. Gene.*, Vol.4, pp.85-99, Jan. 2010.
- [25] A. Hasanzadeh, O.C. Onar, H. Mokhtari, A. Khaligh, "A proportional-resonant controller-based wireless control strategy with a reduced number of sensors for parallel-operated UPSs", *IEEE Trans. on Pow. Deli.*, Vol.25, No.1, pp.468-478, Jan. 2010.
- [26] S. Okada, T. Nunokawa, T. Takeshita, "Digital control scheme of single-phase uninterruptible power supply", *IEEE/INTELEC*, pp.1-6, Oct. 2009.
- [27] T.F. Wu, Y.E. Wu, H.M. Hsieh, Y.K. Chen, "Current weighting distribution control strategy for multi-inverter systems to achieve current sharing", *IEEE Tran. On Pow. Elet.*, Vol.22, No.1, pp.160-168, Jan. 2007.
- [28] J. Faiz, G. Shahgholian, M. Ehsan, "Stability analysis and simulation of the single-phase voltage source UPS inverter with two-stage cascade output filter", *Euro. Trans. Electr. Power*, No.18, pp.29-49, 2008.
- [29] R.A. Gannett, "control strategies for high power four leg voltage source inverters", Master thesis, Blacksburg, Virginia, July 2001.
- [30] F. Blaabjerg, J.K. Pedersen, P. Thøgersen, "Improved modulation techniques for PWM-VSI drives", *IEEE Trans. On Indu. Elect.*, Vol.44, No.1, pp.87-95, Feb. 1997.
- [31] F. Kamran, T.G. Habetler, "A novel on line UPS with universal filtering capabilities", *IEEE Tran. On Pow. Ele.*, Vol.13, No.3, pp.410-418, May 1998.
- [32] K. Zhou, D. Wang, "Digital repetitive learning controller for three phase CVCF PWM inverter", *IEEE Trans. On Indu. Elect.*, Vol.48, No.4, pp.820-830, Aug. 2001.
- [33] J. Faiz, G. Shahgholian, "Study and simulation of a single phase UPS inverter with rectifier load", *ECTI-CON*, pp.417-420, May 2006.
- [34] G. Shahgholian, "Analysis and simulation of single and three-phase uninterruptible power supply (UPS)", Ph.D Thesis, Islamic Azad University – Science and Research Branch, Tehran, Iran, 2006.
- [35] H. Deng, R.Oruganti, D.Srinivasan, "Modeling and control of single phase UPS inverters: a survey", *IEEE/PEDS*, Vol.2, pp.848-853, November 2005.
- [36] O. Kukrer, H. Komurcugil, "Deadbeat control method for single phase UPS inverters with compensation of computation delay", *IEE Proc., Electr., Pow. Appl.*, Vol.146, No.1, pp.123-128, Jan. 1999.

- [37] H. Komurcugil, O. Kukrer, A. Doganalp, "Optimal control for single phase UPS inverters based on linear quadratic regulator approach", *IEEE/SPEEDAM*, pp.S8-24-S8-29, May 2006.
- [38] V.F. Montagner, E.G. Carati, H.A. Grundling, "An adaptive linear quadratic regulator with repetitive controller applied to uninterruptible power supplies", *IEEE/IAC*, Vol.4, pp.2231-2236, Oct. 2000.
- [39] P.C. Loh, D.G. Holmes, "Analysis of multi loop control strategies for LC/CL/LCL filtered voltage source and current source inverters", *IEEE Trans. On Indus. Applic.*, Vol.41, No.2, pp.644-654, March/April 2005.
- [40] M. Kojima, K. Hirabayashi, Y. Kawabata, E.C. Ejiogu, T. Kawabata, "Novel vector control system using deadbeat controlled PWM inverter with output LC filter", *IEEE Trans. On Ind. Appl.*, Vol.40, No.1, pp.162-169, Jan./Feb. 2004.
- [41] Y. Liu, Y. Xing, L.Huang, M. Sakane, "Progressively converging deadbeat control for UPS inverter", *IEEE/APEC*, pp.355-361, March 2006.
- [42] Y.Y. Tzou, R.S. Ou, S.L Jung, M.Y. Chang, "High performance programmable ac power source with low harmonic distortion using DSP based repetitive control technique", *IEEE Trans. On Pow. Electr.*, Vol.12, No.4, pp.715-725, July 1997.
- [43] Z. He, M. Li, Y. Xing, "Core techniques of digital control for UPS", *IEEE/ICIT*, pp.546-551, Dec. 2005.
- [44] S. Buso, S. Fasolo, P. Mattavelli, "Uninterruptible power supply multi loop control employing digital predictive voltage and current regulators", *IEEE Trans. On Indu. Appl.*, Vol.37, No.6, November 2001.
- [45] M.P. Kazmierkowski, L. Malesani, "Current control techniques for three phase voltage source PWM converters: A survey", *IEEE Trans. On Indus. Electr.*, Vol.45, No.5, pp.691-703, Oct. 1998.
- [46] X. Sun, M.H.L. Chow, F.H.F. Leung, D.X.Y. Wang, Y.S. Lee, "Analogue implementation of a neural network controller for UPS inverter applications", *IEEE Trans. On Pow. Elec.*, Vol.17, No.3, pp.305-307, May 2002.
- [47] Y. Qin, S. Du, "A novel adaptive hysteresis band current control using a DSP for a power factor corrected on-line UPS", *IEEE/IECON*, Vol.1, pp.208-212, November 1997.
- [48] T.S. Lee, S.J. Chiang, J.M.Chang, "H_∞ loop shaping controller designs for the single phase UPS inverters", *IEEE Trans. On Pow. Elec*, Vol.16, No.4, pp.473-481, July 2001.
- [49] N. M.A.Rahim, "Hierarchical fuzzy logic control for a single phase voltage source ups inverter", *IEEE/IECON*, Vol.1, pp.262-267, Nov. 2002.
- [50] G.H.Bode, P.C.Loh, M.J.Newman, D.G.Holmes, "An improved robust predictive current regulation algorithm", *IEEE Trans. On Indu. Appl.*, Vol.41, No.6, pp.1720-1734, Nov/Dec. 2005.
- [51] Y.Q.Chen, K.L.Moore, V.Bahl, "Learning feedforward control using a dilated B-spline network: frequency domain analysis and design", *IEEE Tran. On Neur.*, Vol.15, No.2, pp.355-, March 2004.
- [52] K.Zhou, D.Wang, "Digital repetitive learning controller for three phase CVCF PWM inverter", *IEEE Trans. On Indu. Elec.*, Vol.48, No.4, pp.820-831, Aug. 2001.
- [53] M.Pande, G.Joos, H.Jin, "Output voltage integral control technique for compensating non-ideal dc buses in voltage source inverters", *IEEE Trans. On Pow. Elec.*, Vol.12, No.2, pp.302-310, March 1997.
- [54] C.Rech, H.Pinheiro, H.A.Grundling, H.L.Hey, J.R.Pinheiro, "A modified discrete control law for UPS applications", *IEEE Trans. On Pow. Elec.*, Vol.18, No.5, pp.1138-1145, Sept. 2003.
- [55] W.Baocheng, W.Weiyang, S.Xiaofeng, W.Kum, "DC to AC inverter with improved one cycle control", *IEEE*, pp.1418-1421, 2003.
- [56] J.M.Guerrero, L.G.Vicufia, J.Matas, J.Miret, "Steady state invariant frequency control of parallel redundant uninterruptible power supplies", *IEEE/IECON*, Vol.1, PP.274-277, 2002.
- [57] G.J.Su, D.J.Adams, L.M.Tolbert, "Comparative study of power factor correction converters for single phase half-bridge inverters", *IEEE/PESC*, Vol.2, pp.995-1000, June 2001.
- [58] T.J.Liang, J.L.Shyu, "Improved DSP controlled online UPS system with high real output power", *IEE Proc. Pow. Appl.*, Vol.151, No.1, pp.121-127, Jan. 2004.
- [59] J.H.Choi, J.H.Kim, "A bidirectional UPS with the performance of harmonic and reactive power compensation", *IEEE/PEDS*, Vol.1, pp.323-328, 1997.
- [60] T.Haneyoshi, A.Kawamura, R.G.Hoft, "Waveform compensation of PWM inverter with cyclic fluctuating loads", *IEEE Trans. On Ind. Appl.*, Vol.24, No.4, pp.582-589, July/Aug. 1988.
- [61] Y.Y.Tzou, S.L.Jung, H.C.Yeh, "Adaptive repetitive control of PWM inverters for very low THD AC-voltage regulation with unknown loads", *IEEE Trans. On Pow. Electr.*, Vol.14, No.5, pp.973-981, Sep. 1999.
- [62] H.A.Grundling, E.G.Carati, J.R.Pinheiro, "A robust model reference adaptive controller for UPS applications", *IEEE/IECON*, Vol.2, pp.901-905, Nov. 1997.
- [63] J.Hatonen, "Issues of algebra and optimality in iterative learning control", Master Thesis, Oulu, Finland 2004.
- [64] L.B.Brahim, M.Benammar, M.A.Alhamadi, "A new iterative learning control method for PWM inverter current regulation", *IEEE/PEDS*, Vol.2, pp.1460-1465, Nov. 2003.
- [65] W.Weil, S.K.Panda, J.X.Xu, "Control of high performance dc-ac inverters using iterative learning control", *IEEE/TENCON*, Vol.D, pp.132-135, Nov. 2004.
- [66] S.Cong, R.Song, "An improved B-spline fuzzy neural network controller", *IEEE/WCICA*, Vol.3, pp.1713-1717, June/July 2000.
- [67] G.J.Menken, "Modularly structured B-spline networks for internal model control", *IEEE/AACC*, Vol.3, pp.2001-2006, June/July 1997.
- [68] K.W.E.Cheng, H.Y.Wang, D.Sutanto, "Adaptive B-spline network control for three phase PWM ac-dc voltage source converter", *IEEE/PEDS*, Vol.1, pp.467-472, July 1999.
- [69] L.Xinchun, C.Xikum, K.Yong, D.Shanxu, C.Jian, "Research on parallel operation of three phase UPS inverters", *IEEE/INELEC*, pp.195-199, Sep. 2002.
- [70] A.Kawamura, R.Chuarayratip, T.Haneyoshi, "Deadbeat control of PWM inverter with modified pulse patterns for uninterruptible power supply", *IEEE tran. On Ind. Elec.*, Vol.35, No.2, pp.295-300, 1988.
- [71] J.M.Guerrero, L.G.Vicuna, J.Matas, J.Miret, M.Castilla, "A high performance DSP controller for parallel operation of online UPS systems", *IEEE/APEC*, Vol.1, pp.463-469, Feb. 2004.
- [72] J.F.Chen, C.L.Chu, "Combination voltage controlled and current controlled PWM inverters for UPS parallel operation", *IEEE Trans. On Pow. Electr.*, Vol.10, No.5, pp.547-558, 1995.
- [73] A.Tuladhar, H.Jin, T.Unger, K.Mauch, "Parallel operation of single phase inverter modules with no control interconnections", *IEEE/APEC*, Vol.1, pp.94-100, Feb. 1997.
- [74] R.O.Caceres, I.Barbi, "A boost dc-ac converter: analysis, design and experimentation", *IEEE Trans. On Electr.*, Vol.14, No.1, pp.134-141, Jan. 1999.
- [75] D.Shanxu, M.Yu, X.Jian, K.Yong, C.Jian, "Parallel operation control technique of voltage source inverters in UPS", *IEEE/PEDS*, Vol.2, pp.883-887, July 1999.
- [76] L.Xinchun, C.Xikum, K.Yong, D.Shanxu, C.Jian, "Research on parallel operation of three phase UPS inverters", *IEEE/INELEC*, pp.195-199, Sep. 2002.
- [77] G. Kecun, D. Yuxing, "DSP control method of single-phase inverters for UPS applications", *IEEE/CCC*, pp.670-672, Jun. 2007.
- [78] G.Alarcon, V.Cardenas, S.Ramirez, N.Visairo, C.Nufiez, M.Oliver, H.S.Ramirez, "Non linear passive control with inductor current feedback for an UPS inverter", *IEEE/PESC*, Vol.3, pp.1414-1418, June 2000.
- [79] M.E.Fraser, C.D.Manning, "Performance of average current mode controlled PWM ups inverter with high crest factor load", *IEE/PEVSD*, No.399, pp.661-667, Oct. 1994.
- [80] P.A.Dahono, "New current controllers for single phase full bridge inverters", *IEEE/POWERCON*, pp.1757-1762, November 2004.
- [81] H.Wu, D.Lin, D.Zhang, K.Yao, J.Zhang, "A current mode control technique with instantaneous inductor current feedback for UPS inverters", *IEEE/APEC*, Vol.2, pp.951-957, March 1999.

- [82] P.Enjeti, J.F.Lindsay, P.D.Ziogas, M.H.Rashid, "New current control scheme for PWM inverters", *IEE Proc.*, Vol.135, No.4, pp.172-179, July 1988.
- [83] D.G.Holmes, D.A.Martin, "Implementation of a direct digital predictive current controller for single and three phase voltage source inverters", *IEEE*, pp.906-913, 1996.
- [84] E.A.A.Coelho, P.C.Cortizo, F.D.Garcia, "Small signal stability for parallel connected inverters in stand alone ac supply systems", *IEEE Tran. On Inds. Appl.*, Vol.38, No.2, pp.533-542, 2002.
- [85] G.E.Valderrama, A.M.Stankovic, P.Mattavelli, "Dissipative based adaptive and robust control of UPS unbalanced operation", *IEEE Trans. On Pow. Ele.*, Vol.18, No.4, pp.1056-1062, July 2003.
- [86] K. Zhou, J. Chen, R. Xiong, Y. Kang, K. Zhang, J. Xiong, M. Kchikach, "Study on the mechanism of output waveform distortion & distortion suppression of PWM VSI with load", *IEEE/PESC*, Vol.2, pp.2083-2089, 1998.
- [87] Y. Wei, Z. Chongfeng, C. Min, Q. Zhaoming, "Analysis and research of a multiple loop control strategy for single phase UPS inverters", *IEEE/PEDS*, pp.628-632, Nov. 2005.
- [88] N.M.A. Rahim, J.E. Quatcoe, "Analysis and design of a multiple feedback loop control strategy for single phase voltage source UPS inverters", *IEEE Tran. On Pow. Elec.*, Vol.11, No.4, pp.532-541, July 1996.
- [89] H. Gueldner, H. Wolf, N. Blacha, "Single phase UPS inverter with variable output voltage and digital state feedback control", *IEEE/ISIE*, Vol.2, pp.1089-1094, June 2001.
- [90] L.Samaranayake, M.Leksell, S.Alahakoon, "State feedback controller for distributed systems with non-deterministic time delays", *IEEE/ISIC*, pp.140-145, Oct. 2003.
- [91] Z.Kai, K.Yong, X.Jian, Z.Hui, C.Jian, "Study on an inverter with pole assignment and repetitive control for UPS application", *IEEE/PIEMC*, Vol.2, pp.650-653, Aug. 2000.
- [92] Y.B.Byun, K.Joe, S.Park, C.Kim, "DSP control of three phase voltage source UPS inverter with software controlled harmonic conditioners", *IEEE/INTELEC*, pp.195-200, Oct. 1997.
- [93] C.A.Ayres, I.Barbi, "A family of converters for UPS production burn in energy recovery", *IEEE Tran. On Pow. Ele.*, Vol.12, No.4, pp.615-622, 1997.
- [94] H.L. Jou, J.C. Wu, C. Tsai, K.D. Wu, M.S. Huang, "Novel line interactive uninterruptible power supply", *IEE Proc., Electr., Pow. Appl.*, Vol.151, No.3, pp.359-364, May 2004.
- [95] J.M.Guerrero, L.G.Vicuna, J.Miret, J.Matas, M.Castilla, "A nonlinear feed forward control technique for single phase UPS inverters", *IEEE/IECON*, Vol.1, pp.257-261, Nove. 2002.
- [96] D.E. Kim, D.C. Lee, "Inverter output voltage control of three-phase UPS systems using feedback linearization", *IEEE/IECON*, pp.1737-1742, November 2007.
- [97] B.H.Kwon, J.H.Choi, T.W.Kim, "Improved single phase line interactive UPS", *IEEE Trans. On Ind. Elect.*, Vol.48, No.4, pp.804-811, Aug. 2001.
- [98] S.L.Jung, Y.Y.Tzou, "Discrete sliding mode control of a PWM inverter for sinusoidal output waveform synthesis with optimal sliding curve", *IEEE Trans. On Pow. Electron.*, Vol.11, No.4, pp.567-577, 1996.
- [99] N.Vazquen, J.Alvarez, C.Aguilar, J.Arau, "Some critical aspects in sliding mode control design for the boost inverter", *IEEE/CIEP*, pp.76-81, 1998.
- [100] J.F.Silva, S.S.Paulo, "Fixed frequency sliding modulator for current mode PWM inverters", *IEEE/PESC*, pp.623-629, June 1993.
- [101] H.Pinherio, A.S.Martins, J.R.Pinherio, "A sliding mode controller in single phase voltage source inverters", *IEEE/IECON*, Vol.1, pp.394-398, Sept. 1994.
- [102] L.Xinchun, F.Feng, D.Shanxu, K.Yong, C.Jian "Modeling and stability analysis for two paralleled UPS with no control interconnection", *IEEE/IEEMDC*, Vol.3, pp.1772-1776, June 2003.
- [103] D.S.Xu, K.Yong, C.Jian, "An algorithm for the output waveform compensation of SPWM inverters based on fuzzy - repetitive control", *Elec. Engi.*, Vol.55, No.3-4, pp.64-70, 2004.
- [104] W.Guo, S.Duan, K.Xuejuan, Y.Kang, J.Chen, "A modified deadbeat control for single-phase voltage-source PWM inverters based on asymmetric regular sample", *IEEE/PESC*, Vol.2, pp.962-967, 2001.
- [105] Y.Qin, S.Du, "To design optimized fuzzy and digital PID controllers for single phase power factor per regulator genetic algorithm approach", *IEEE/IAS*, Vol.2, pp.791-796, 1997.
- [106] E.D.Bolat, K.Erakan, S.Postalcioğlu, "Using current mode fuzzy gain scheduling of PI controller for UPS inverter", *IEEE/EUROCON*, Vol.2, pp.1505-1508, Nov. 2005.
- [107] B.R.Lin, C.Hua, "Uninterruptible power supply with fuzzy logic approach", *IEEE/IECON*, Vol.2, pp.1123-128, Nov. 1993.
- [108] L Jian, K. Yong, C. Jian, "DSP based fuzzy tuning repetitive control of an inverter", *IEEE/PEDS*, Vol.1, pp.384-389, Oct. 2001.
- [109] X. Sun, D. Xu, F.H.F.Leung, Y.Wang, Y.S.Lee, "Design and implementation of a neural network controlled UPS inverter", *IEEE/IECON*, Vol.2, pp.779-784, Nov. 1999.
- [110] D. Daniolos, M.K.Darwish, P.Mehta, "Optimized PWM inverter control using artificial neural networks", *IEE* Vol.33, No.20, pp.1739-1740, Sep. 1995.
- [111] L.G.Barnes, R.Krishnan, "An adaptive three phase UPS inverter controller", *IEEE/PESC*, Vol.1, pp.473-479, June 1995.
- [112] S. Buso, "Robust control of single phase UPS", *IEEE/APEC*, Vol.2, pp.826-831, Feb. 1997.

Authors' information

Department of Electrical Engineering,
Najafabad Branch, Islamic Azad University,
Najafabad, Esfahan, Iran.



Ghazanfar Shahgholian was born in Esfahan, Iran, on December 7, 1968. He graduated in electrical engineering from Esfahan University of Technology (IUT), Esfahan, Iran, in 1992. He received the M.Sc and PhD in electrical engineering from Tabriz University, Tabriz, Iran in 1994 and from Islamic Azad University, Science and Research Branch, Tehran, Iran, in 2006, respectively.

He is now an associate professor at Department of Electrical Engineering, Faculty of Engineering, Islamic Azad University Najafabad Branch. His teaching and research interests include application of control theory to power system dynamics, power electronics and power system simulation.



Jawad Faiz (M'90-SM'93) received his Ph.D. in Electrical Engineering from the University of Newcastle upon Tyne, England in 1988. He is a Professor in the Department of Electrical and Computer Engineering, Faculty of Engineering, University of Tehran, Tehran, Iran. He is the author of 290 publications in international journals and conference proceedings. Dr Faiz is a

member of Iran Academy of Sciences. His teaching and research interests are switched reluctance and VR motors design, design and modeling of electrical machines, drives, and transformers.



Masoud Jabbari was born in Isfahan, Iran, in 1979. He received his M.S. and Ph.D. in Electrical Engineering from the Isfahan University of Technology (IUT) in 2003 and 2009, respectively. His research interests include soft-switching techniques in high-frequency high power dc-dc and dc-ac converters, power factor corrections, and active power filters. He is now an assistant professor at Department of Electrical Engineering, Faculty of Engineering, Islamic Azad University Najafabad Branch.

Copyright of International Review of Electrical Engineering is the property of Praise Worthy Prize S.r.L. and its content may not be copied or emailed to multiple sites or posted to a listserv without the copyright holder's express written permission. However, users may print, download, or email articles for individual use.